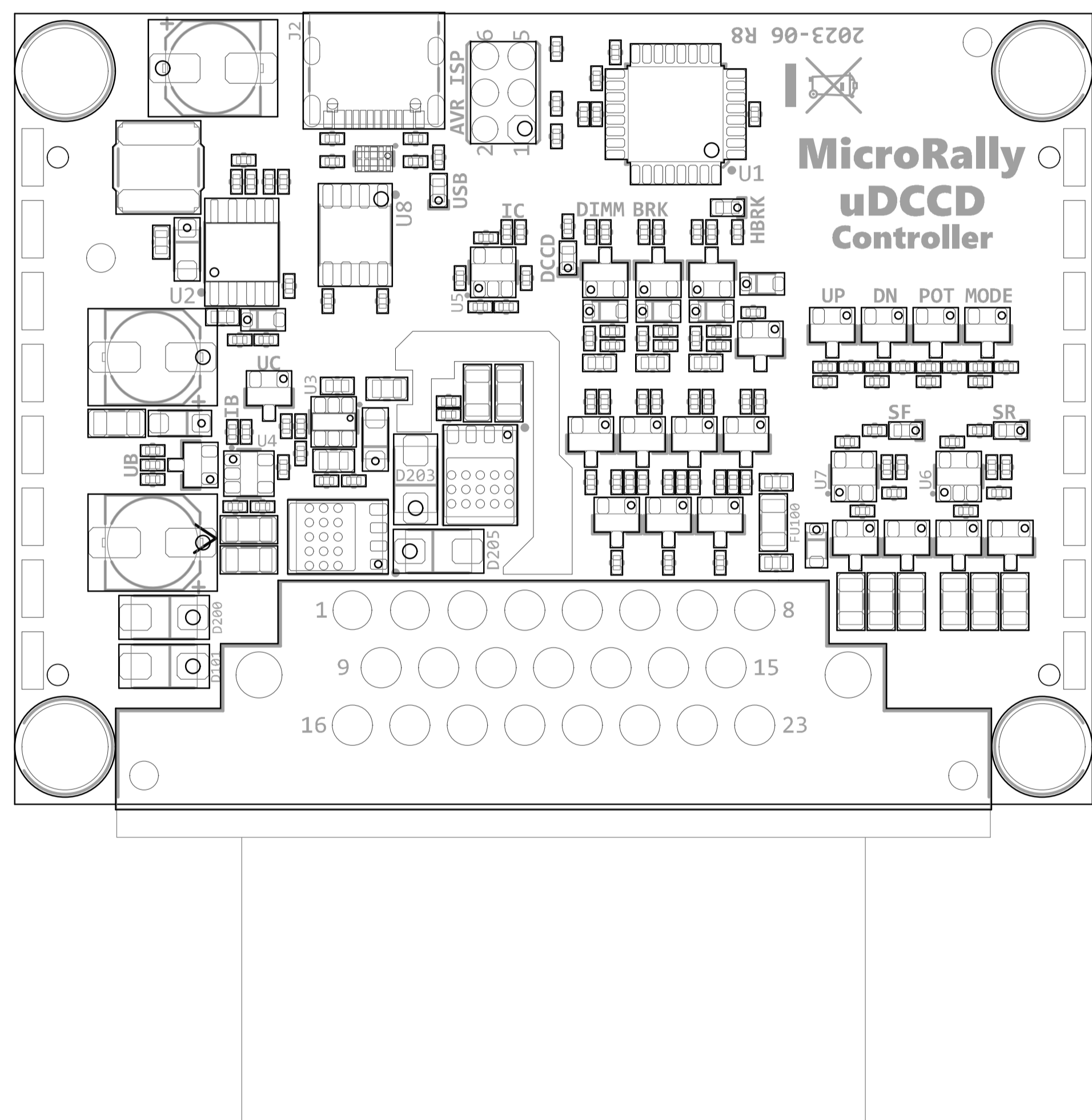


# DESIGN NOTES

## MAIN CONNECTOR

TE AMPSEAL 23 PIN RIGHT ANGLE (776087-x)

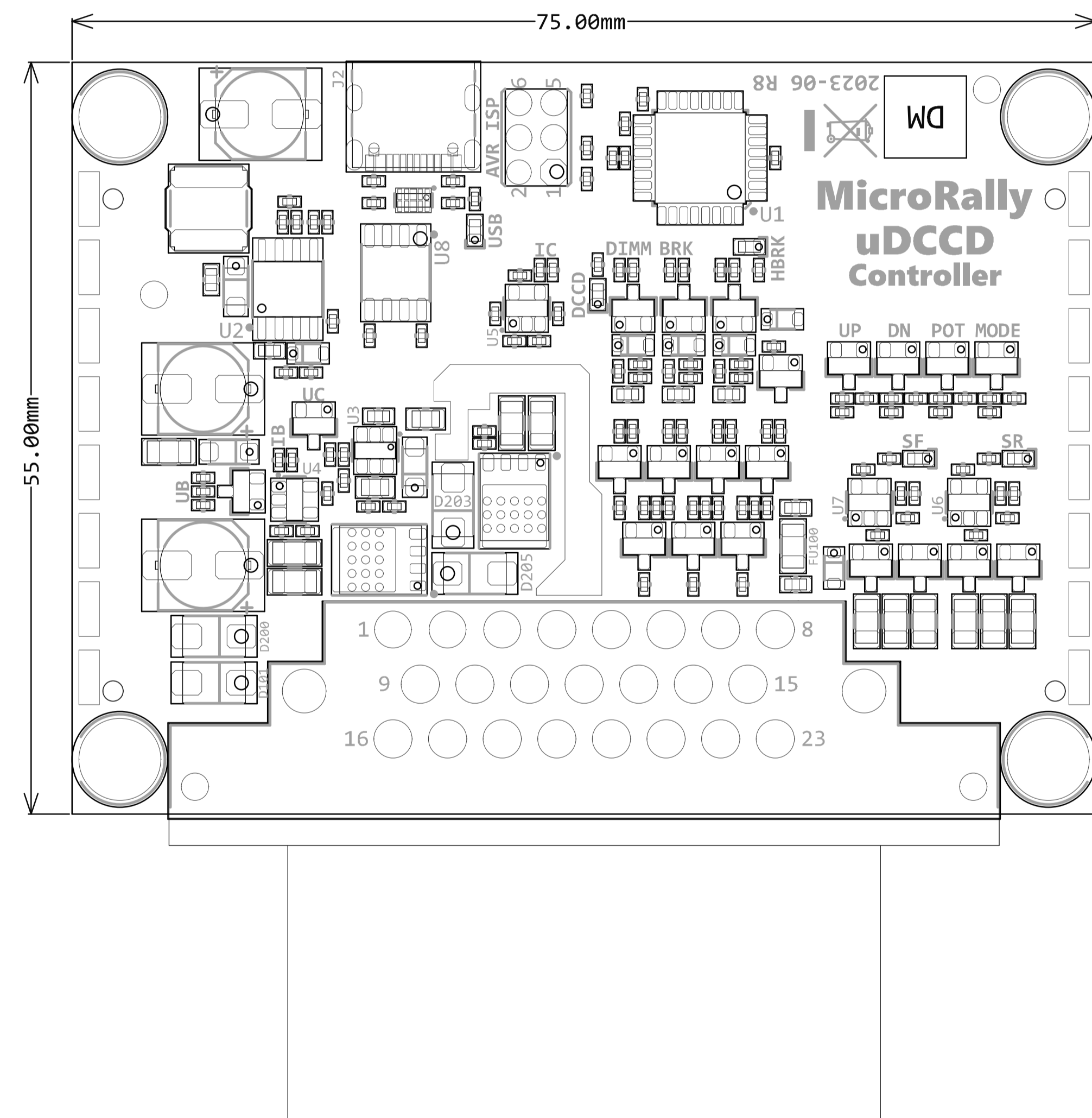
VBAT COIL COIL  GND   D20   D60  D100  5V
IGN  SPDF SPDR  D00   D40   D80   UP
GND  DIMM  BRK  HBRK MODE  POT   DN   GND



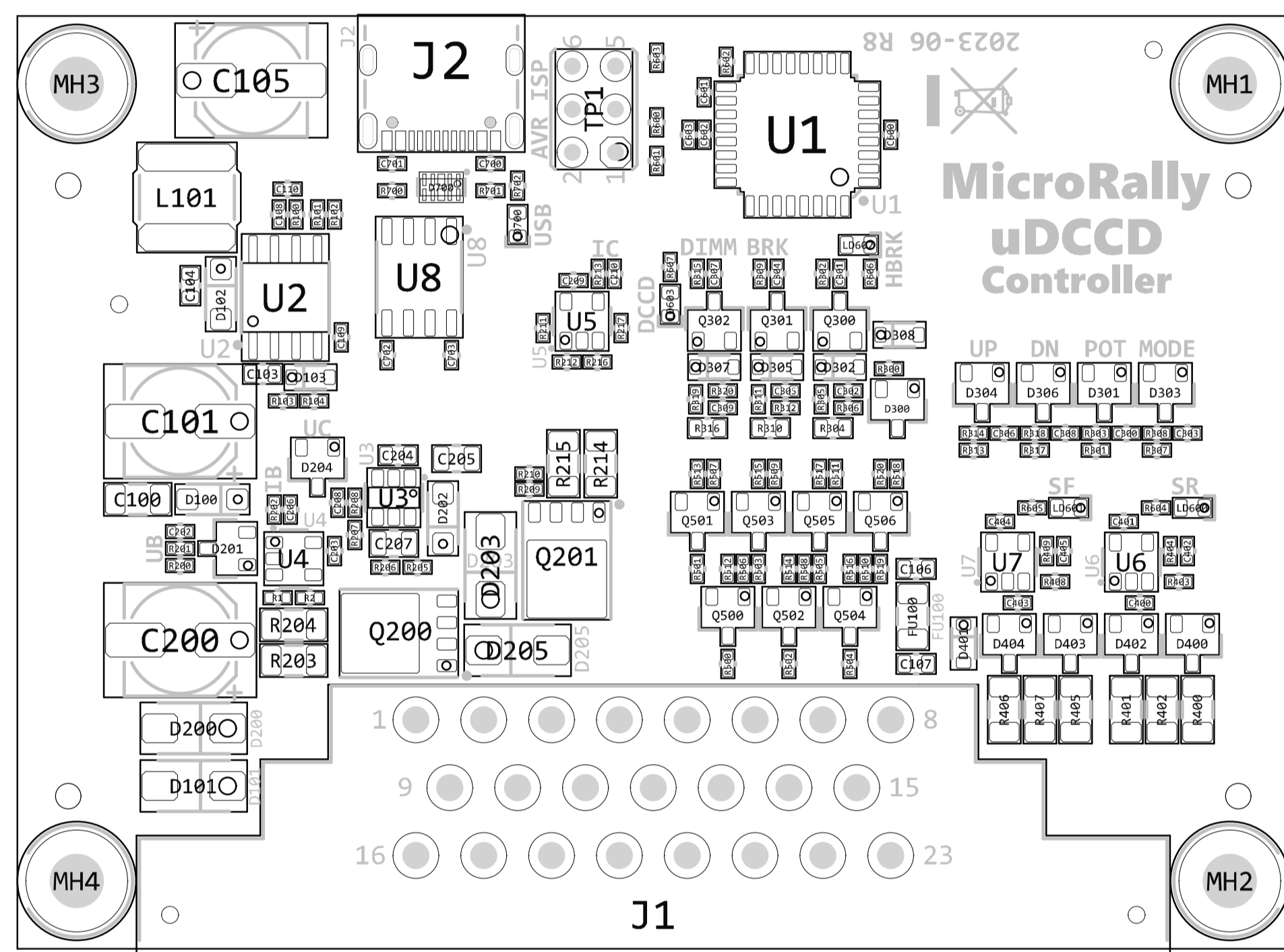
Title: PCB Drawing		<b>MicroRally</b>	
Project: uDCCD Controller			
Revision: R8	Size: A4	Scale: 1:1	Engineer: Andis Zile
File: uDCCD_Controller.PcbDoc			Date: 02.06.2023

# ASSEMBLY NOTES

1) PLACE DATAMATRIX IN DESIGNATED AREA ON TOP SIDE

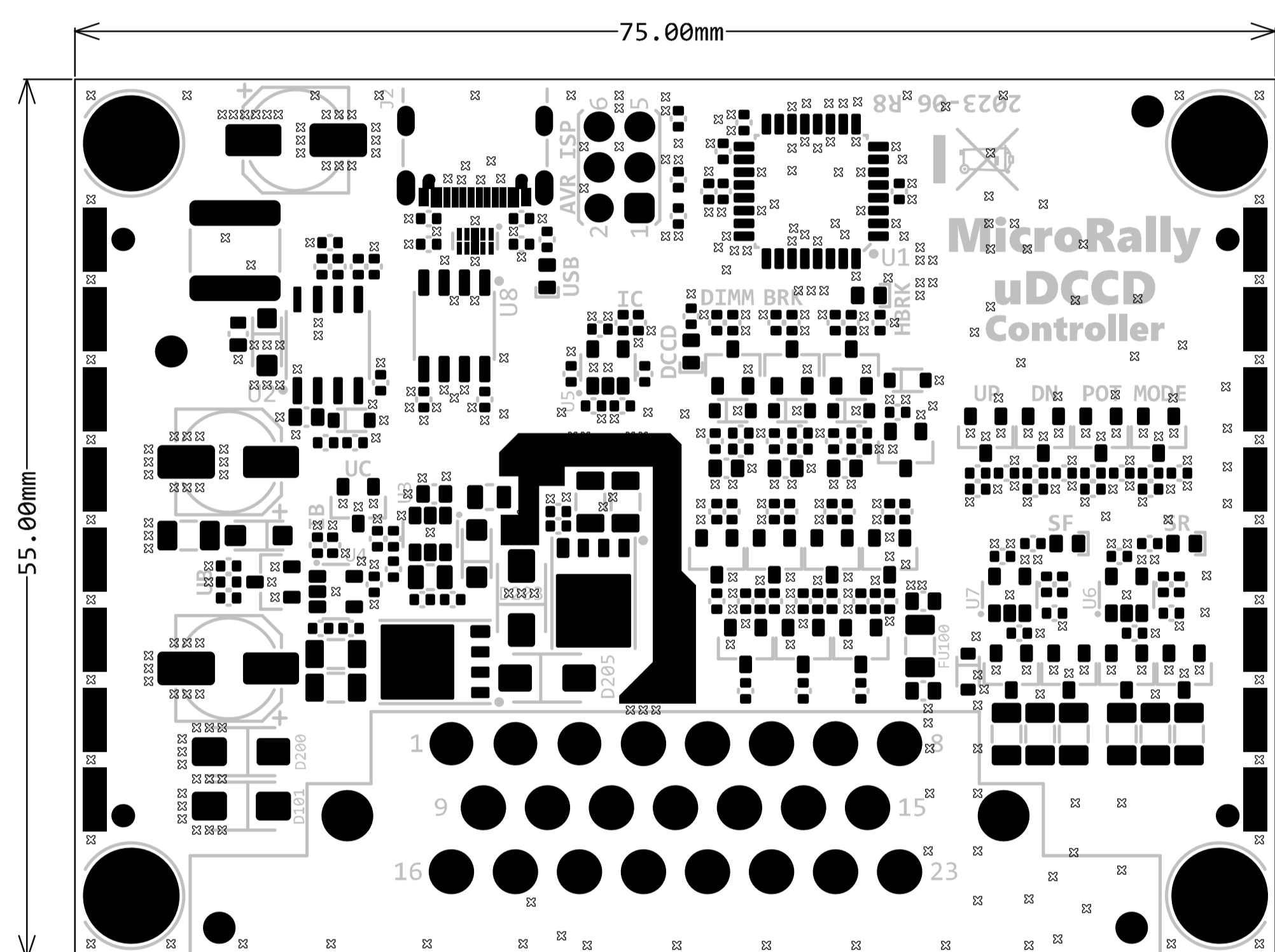


Title: PCB Drawing			<b>MicroRally</b>
Project: uDCCD Controller			
Revision: R8	Size: A4	Scale: 1:1	Engineer: Andis Zile
File: uDCCD_Controller.PcbDoc			Date: 02.06.2023



Title: PCB Drawing			<b>MicroRally</b>
Project: uDCCD Controller			
Revision: R8	Size: A4	Scale: 1:1	Engineer: Andis Zile
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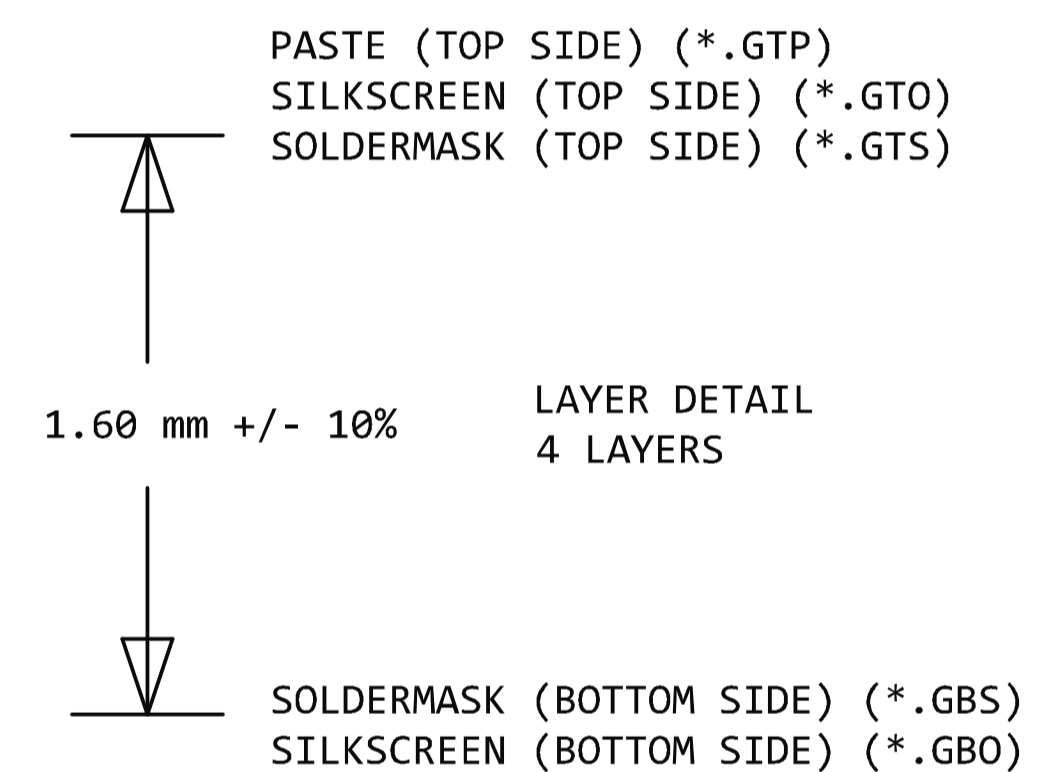
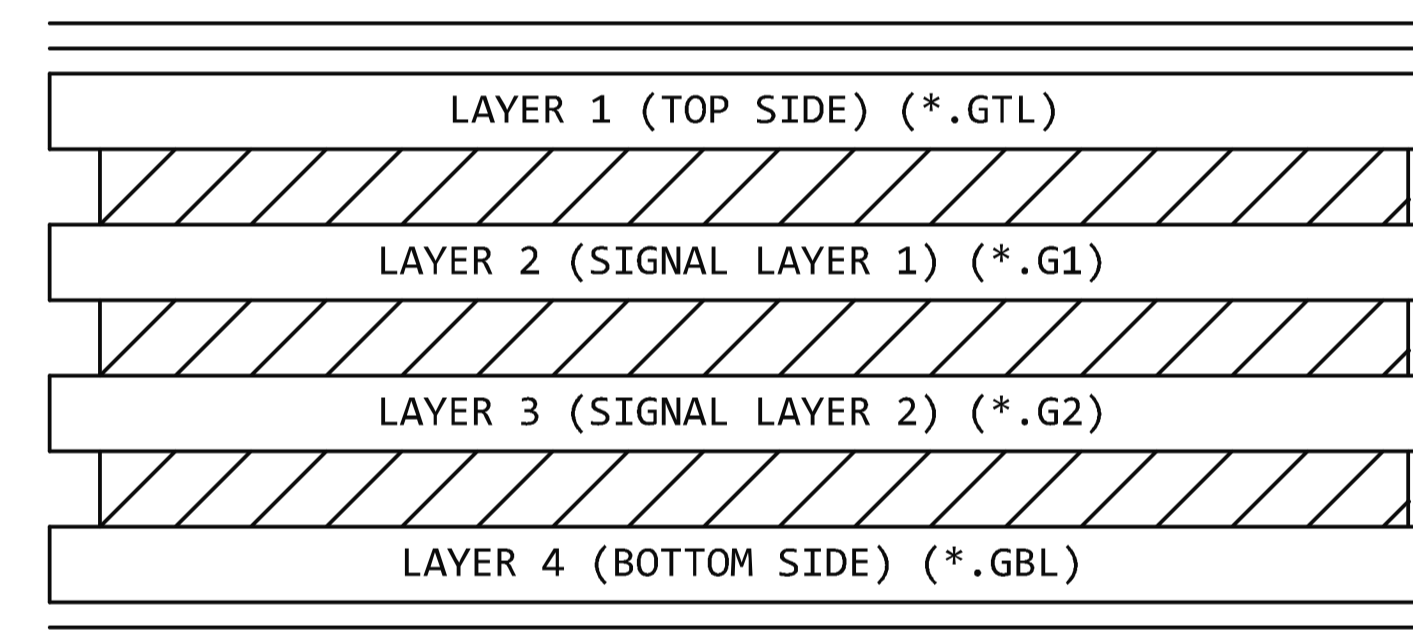
# GERBER NOTES



Symbol	Count	Hole Size	Plated	Hole Type	Via/Pad
⊗	482	0.30mm	PTH	Round	Via
▽	23	1.60mm	PTH	Round	Pad
⊕	6	1.00mm	PTH	Round	Pad
○	4	1.15mm	NPTH	Round	Pad
□	4	3.20mm	PTH	Round	Pad
⊙	2	0.60mm	PTH	Slot	Pad
◻	2	0.60mm	PTH	Slot	Pad
◇	2	0.65mm	NPTH	Round	Pad
☆	2	2.85mm	NPTH	Round	Pad
	527 Total				

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.  
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

1.0 OZ L1  
0.2104 mm Prepreg 7628  
0.5 OZ L2  
1.065 mm Core - FR4  
0.5 OZ L3  
0.2104 mm Prepreg 7628  
1.0 OZ L4



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File: uDCCD_Controller.PcbDoc			Date: 02.06.2023

1

2

3

4

A

A

B

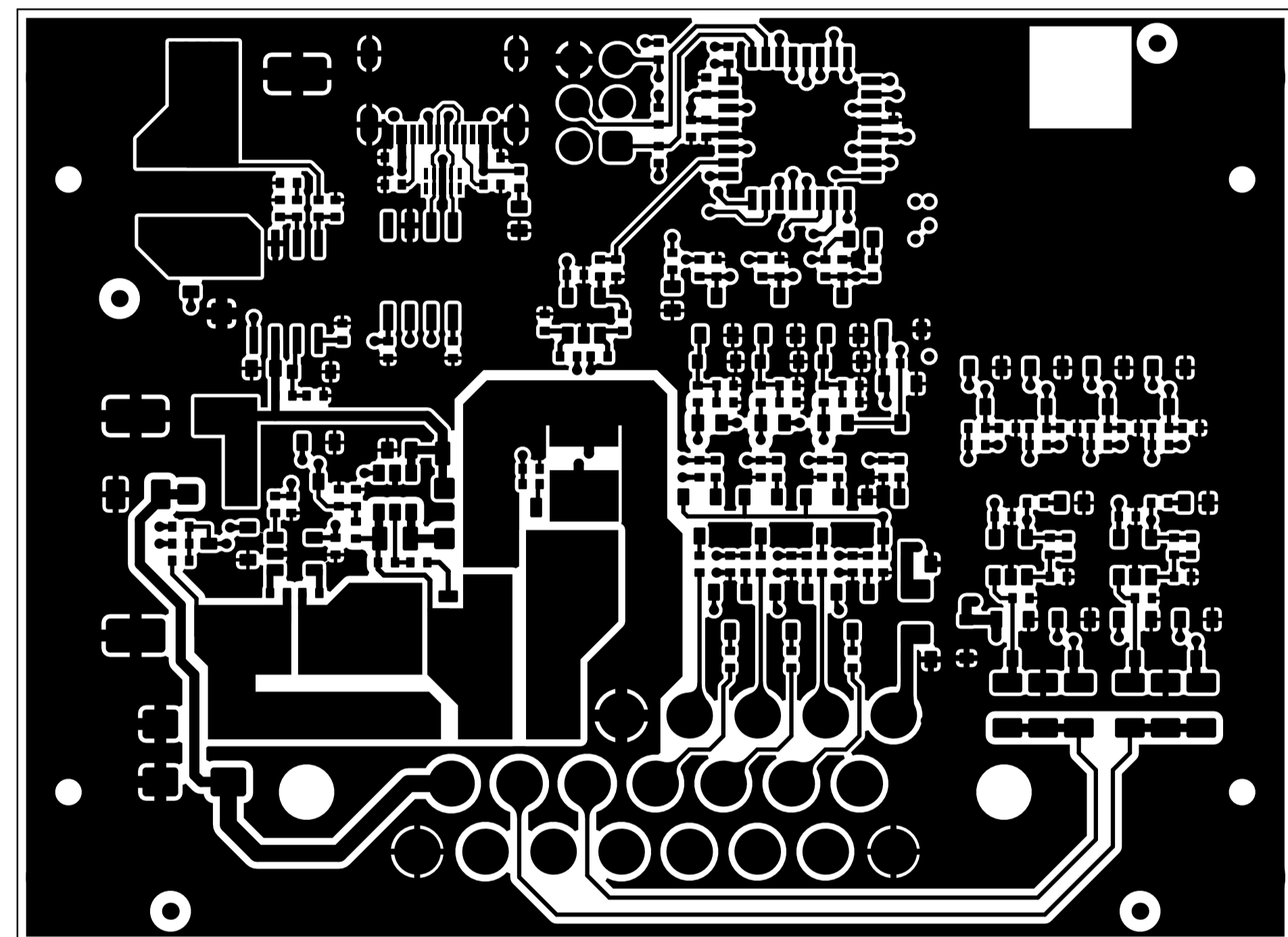
B

C

C

D

D



Title: PCB Drawing			<b>MicroRally</b>
Project: uDCCD Controller			
Revision: R8	Size: A4	Scale: 1:1	Engineer: Andis Zile
File: uDCCD_Controller.PcbDoc			Date: 02.06.2023

1

2

3

4

1

2

3

4

A

A

B

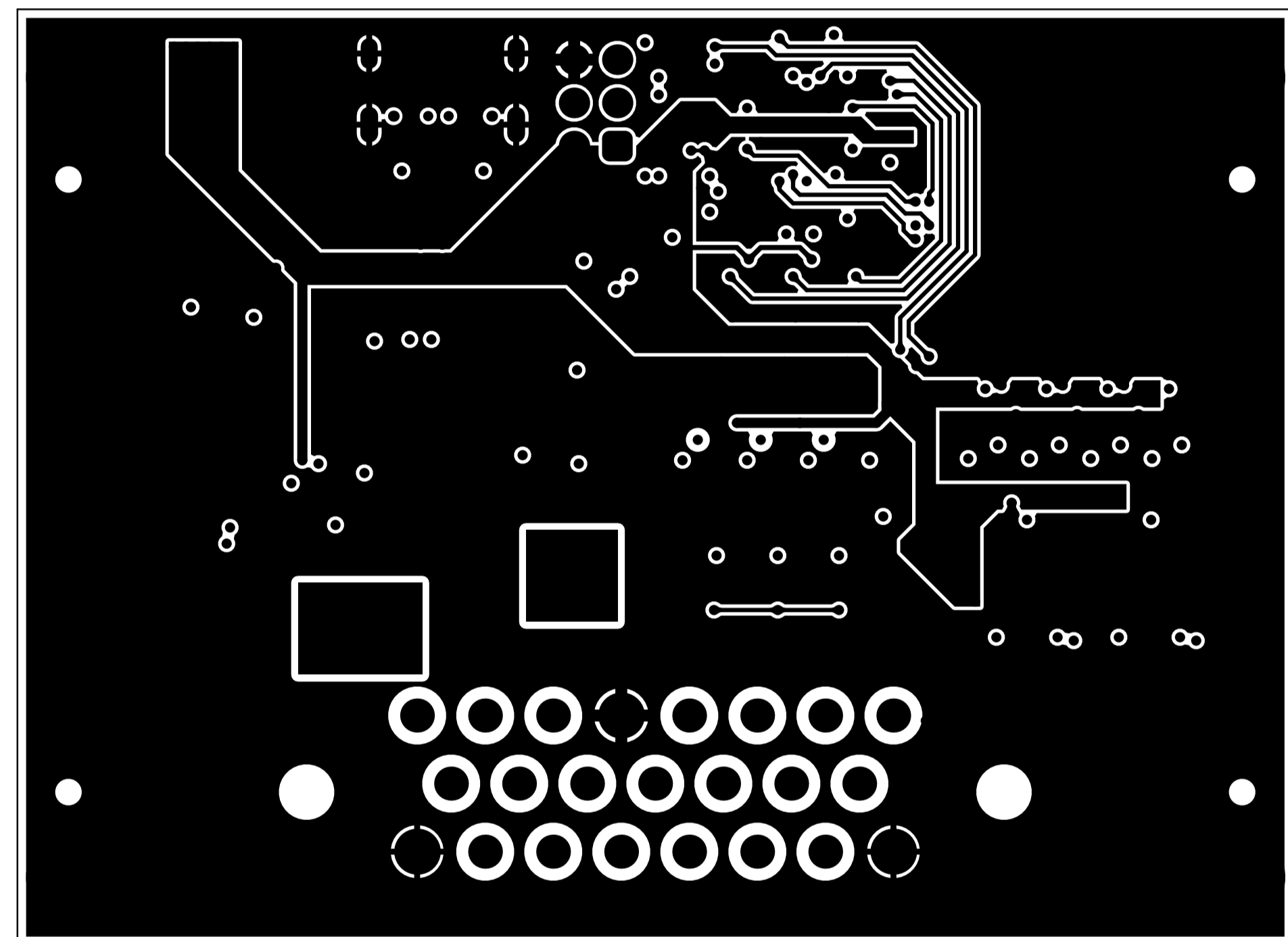
B

C

C

D

D



Title: PCB Drawing			<b>MicroRally</b>
Project: uDCCD Controller			
Revision: R8	Size: A4	Scale: 1:1	Engineer: Andis Zile
File: uDCCD_Controller.PcbDoc			Date: 02.06.2023

1

2

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1

2

3

4

A

A

B

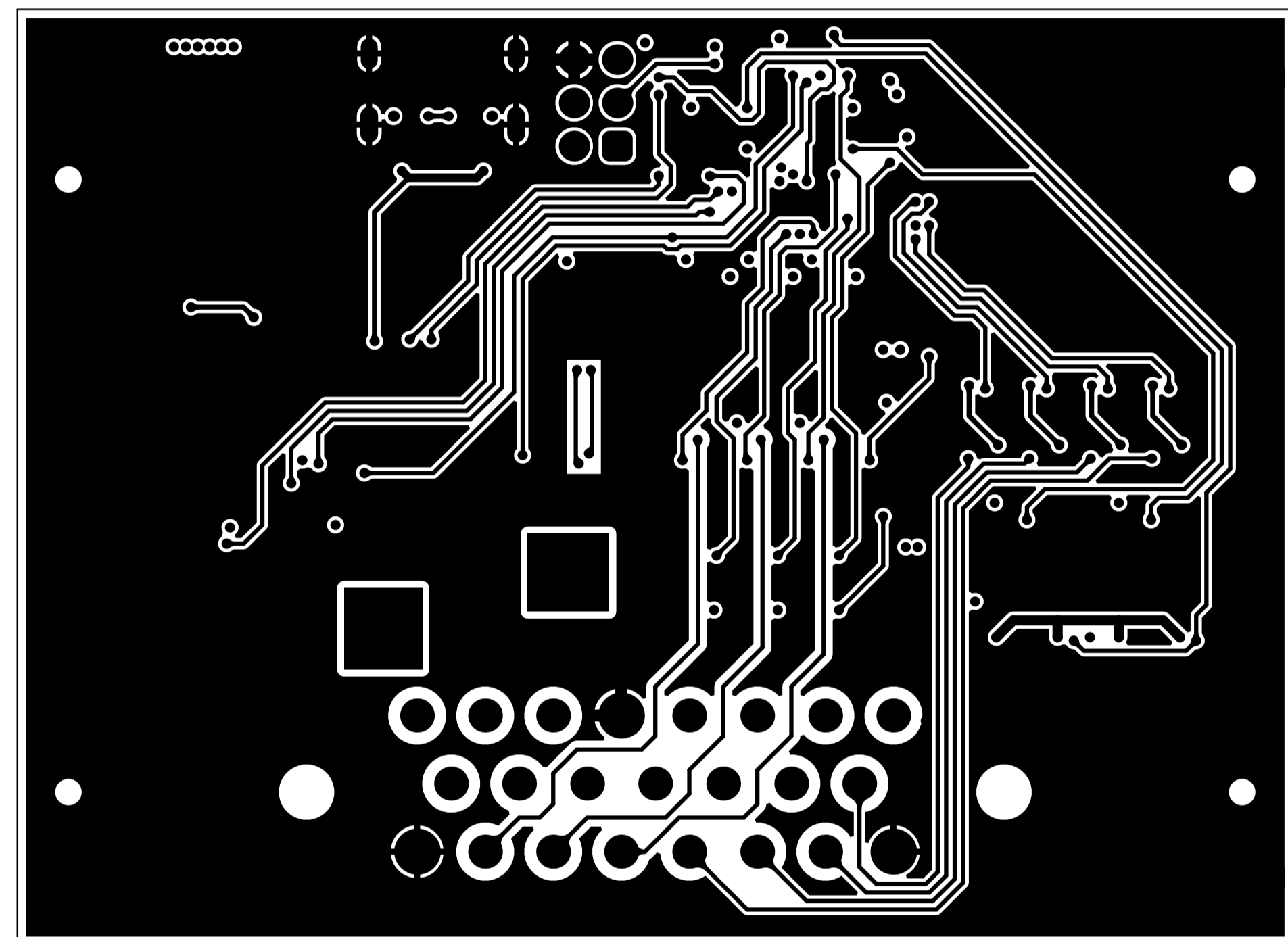
B

C

C

D

D



Title: PCB Drawing			<b>MicroRally</b>
Project: uDCCD Controller			
Revision: R8	Size: A4	Scale: 1:1	Engineer: Andis Zile
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1

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1

2

3

4

A

A

B

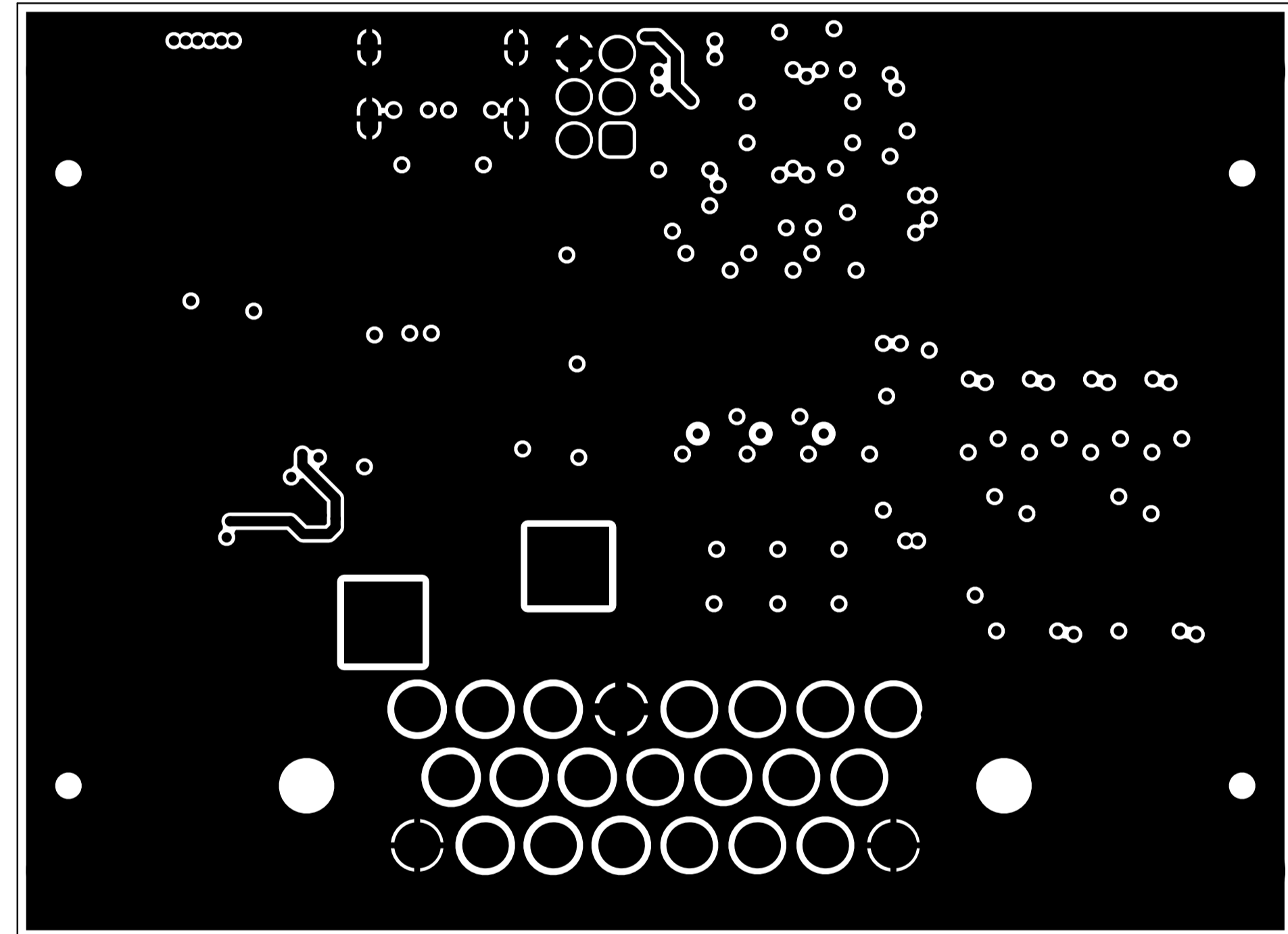
B

C

C

D

D



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1

2

3

4

